Results for "((cosimulation and 'boundary scan protocol' and 'high level modeling')<in>metadata)" e-mail printe
Your search matched **0** documents.
A maximum of **100** results are displayed, **25** to a page, sorted by **Relevance** in **Descending** order.

» Search Options

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New Search

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((cosimulation and 'boundary scan protocol' and 'high level modeling')<in>metadata) Search

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Display Format: ◉ Citation ○ Citation & Abstract

» Key

IEEE JNL	IEEE Journal or Magazine
IEE JNL	IEE Journal or Magazine
IEEE CNF	IEEE Conference Proceeding
IEE CNF	IEE Conference Proceeding
IEEE STD	IEEE Standard

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Results for "((cosimulation and 'boundary scan protocol' and 'reconfigurable hardware')<in>metadata)"
Your search matched **0** documents.
A maximum of **100** results are displayed, **25** to a page, sorted by **Relevance** in **Descending** order.

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New Search

Modify Search

((cosimulation and 'boundary scan protocol' and 'reconfigurable hardware')<in>metad Search

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IEEE JNL	IEEE Journal or Magazine
IEE JNL	IEE Journal or Magazine
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Welcome United States Patent and Trademark Office

Results for "(('high level modeling' and 'boundary scan protocol' and 'reconfigurable hardware')<in>m..." e-mail printe
Your search matched **0** documents.
A maximum of **100** results are displayed, **25** to a page, sorted by **Relevance** in **Descending** order.

» Search Options

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New Search

Modify Search

(('high level modeling' and 'boundary scan protocol' and 'reconfigurable hardware')<in Search

Check to search only within this results set

Display Format: Citation Citation & Abstract

» Key

IEEE JNL	IEEE Journal or Magazine
IEE JNL	IEE Journal or Magazine
IEEE CNF	IEEE Conference Proceeding
IEE CNF	IEE Conference Proceeding
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No results were found.

Please edit your search criteria and try again. Refer to the Help pages if you need assistance revisin search.

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Nothing Found

Your search for **+cosimulation +"boundary scan protocol" +"high level modeling"** did not return any results.

You may want to try an Advanced Search for additional options.

Please review the Quick Tips below or for more information see the Search Tips.

Quick Tips

- Enter your search terms in lower case with a space between the terms.

```
sales offices
```

  You can also enter a full question or concept in plain language.

```
Where are the sales offices?
```

- Capitalize proper nouns to search for specific people, places, or products.

```
John Colter, Netscape Navigator
```

- Enclose a phrase in double quotes to search for that exact phrase.

```
"museum of natural history" "museum of modern art"
```

- Narrow your searches by using a + if a search term must appear on a page.

```
museum +art
```

- Exclude pages by using a - if a search term must not appear on a page.

```
museum -Paris
```

Combine these techniques to create a specific search query. The better your description of the information you want, the more relevant your results will be.

```
museum +"natural history" dinosaur -Chicago
```

Nothing Found

Your search for **+cosimulation +"boundary scan protocol" +"reconfigurable hardware"** did not return any results.

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  "museum of natural history" "museum of modern art"
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- Narrow your searches by using a + if a search term must appear on a page.

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  museum +art
  ```

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museum +"natural history" dinosaur -Chicago
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Nothing Found

Your search for **+"high level modeling" +"boundary scan protocol" +"reconfigurable hardware"** did not return any results.

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  "museum of natural history" "museum of modern art"
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  museum +art
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  museum -Paris
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```
museum +"natural history" dinosaur -Chicago
```

EAST SEARCH

12/5/2006

L#	Hits	Search String	Databases
S1	153	hardware with (co-simulation or cosimulation)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB
S2	292	co-simulation or cosimulation	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB
S3	1154	high level near2 (model$3)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB
S4	37	S2 and S3	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB
S5	1409	S2 or S3	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB
S6	52	S5 and "boundary scan"	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB
S7	17	S5 and ("boundary scan" near2 (interface or protocol))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB
S8	2	S5 and (boundary-scan near2 (interface or protocol))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB
S9	1	S5 and ("boundary scan" with ((reconfigurable or re-configurable) near2 hardware))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB
S10	28	S5 and (interface with((reconfigurable or re-configurable) near2 hardware))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB
S11	2	S5 and ("boundary scan" with translat$3)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB
S12	71	S5 and (protocol with translat$3)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB
S14	11	S5 and ((component or element) near2 wrapper)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB
S15	1	S5 and (wrapper with (memory near2 map))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB
S16	4	S5 and ((component or element) with (memory near2 map))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB
S17	83	S5 and ((component or element) with register)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB
S18	8	S5 and (translat$3 with (shift near2 register))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB
S19	1	S5 and (translat$3 with (shift$3 near2 serially))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB
S20	4	S5 and (shift$3 near2 serially)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB
S21	1	S5 and ((address near2 decoder) with (memory near2 map))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB
S22	8	S5 and (translat$3 with wrapper)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB
S23	1	S5 and ((mapping near2 data) with (addressable near2 memory))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB
S24	156	S4 or S6 or S7 or S8 or S9 or S10 or S11 or S12 or S13 or S14 or S15 or S16 or S18 or S19	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB
S25	34	S17 and S24	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB
S26	156	S24 or S25	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB
S13	14	S5 and ((second or another) near2 protocol)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB
S27	14	S26 and S13	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB
S28	11	S26 and S14	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB
S29	4	S26 and S16	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB
S30	8	S26 and S18	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB
S31	8	S26 and S22	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB
S32	3	6,907,584.pn.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB
S53	8	S36 and (translat$3 with wrapper)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB
S40	1	S36 and ("boundary scan" with ((reconfigurable or re-configurable) near2 hardware))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB
S52	1	S36 and ((address near2 decoder) with (memory near2 map))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB
S42	2	S36 and ("boundary scan" with translat$3)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB
S45	11	S36 and ((component or element) near2 wrapper)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB
S33	292	co-simulation or cosimulation	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB
S51	4	S36 and (shift$3 near2 serially)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB
S49	8	S36 and (translat$3 with (shift near2 register))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB
S47	4	S36 and ((component or element) with (memory near2 map))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB

S39	2	S36 and (boundary-scan near2 (interface or protocol))		US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB
S43	71	S36 and (protocol with translat$3)		US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB
S41	28	S36 and (interface with((reconfigurable or re-configurable) near2 hardware))		US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB
S34	1154	high level near2 (model$3)		US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB
S35	37	S33 and S34		US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB
S54	1	S36 and ((mapping near2 data) with (addressable near2 memory))		US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB
S56	34	S48 and S55		US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB
S55	156	S35 or S37 or S38 or S39 or S40 or S41 or S42 or S43 or S44 or S45 or S46 or S47 or S49 c		US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB
S57	156	S55 or S56		US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB
S44	14	S36 and ((second or another) near2 protocol)		US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB
S37	52	S36 and "boundary scan"		US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB
S50	1	S36 and (translat$3 with (shift$3 near2 serially))		US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB
S48	83	S36 and ((component or element) with register)		US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB
S46	1	S36 and (wrapper with (memory near2 map))		US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB
S36	1409	S33 or S34		US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB
S38	17	S36 and ("boundary scan" near2 (interface or protocol))		US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB
S58	2		20060117274	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB
S59	311	co-simulation or cosimulation		US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB
S60	1252	high level near2 (model$3)		US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB
S61	41	S59 and S60		US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB
S62	1522	S59 or S60		US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB
S63	53	S62 and "boundary scan"		US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB
S64	17	S62 and ("boundary scan" near2 (interface or protocol))		US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB
S65	2	S62 and (boundary-scan near2 (interface or protocol))		US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB
S67	28	S62 and (interface with((reconfigurable or re-configurable) near2 hardware))		US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB
S66	1	S62 and ("boundary scan" with ((reconfigurable or re-configurable) near2 hardware))		US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB
S68	2	S62 and ("boundary scan" with translat$3)		US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB
S80	1	S62 and ((mapping near2 data) with (addressable near2 memory))		US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB
S69	73	S62 and (protocol with translat$3)		US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB
S82	36	S74 and S81		US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB
S70	15	S62 and ((second or another) near2 protocol)		US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB
S71	11	S62 and ((component or element) near2 wrapper)		US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB
S73	4	S62 and ((component or element) with (memory near2 map))		US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB
S72	1	S62 and (wrapper with (memory near2 map))		US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB
S74	89	S62 and ((component or element) with register)		US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB
S75	8	S62 and (translat$3 with (shift near2 register))		US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB
S76	1	S62 and (translat$3 with (shift$3 near2 serially))		US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB
S77	4	S62 and (shift$3 near2 serially)		US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB
S78	1	S62 and ((address near2 decoder) with (memory near2 map))		US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB
S79	8	S62 and (translat$3 with wrapper)		US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB
S81	164	S61 or S63 or S64 or S65 or S66 or S67 or S68 or S69 or S70 or S71 or S72 or S73 or S75 c		US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB
S83	164	S81 or S82		US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB
S85	0	S84 and (co-simulation or cosimulation)		US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB
S86	0	S84 and "boundary scan"		US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB
S87	0	S84 and protocol		US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB
S88	0	S84 and wrapper		US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB
S84	2	7,085,706.pn.		US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB
S89	17	S83 and (S64 or S65)		US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB

EAST SEARCH

12/5/2006

Results of search set S91:

Document Kind Codes	Title	Issue Date	Current OR	Abstract
US 20060117274 A1	Behavior processor system and method	20060601	716/1	
US 20060083205 A1	Method and system for wireless networking using coordinated dynamic spectrum access	20060420	370/338	
US 20060015674 A1	Self-booting software defined radio module	20060119	711/101	
US 20050267729 A1	Extensible memory architecture and communication protocol for supporting multiple devices i	20051201	703/25	
US 20050256691 A1	Post initial microcode load co-simulation method, system, and program product	20051117	703/14	
US 20050229170 A1	Optimized system-level simulation	20051013	717/165	
US 20050228630 A1	VCD-on-demand system and method	20051013	703/19	
US 20050228628 A1	System-level simulation of interconnected devices	20051013	703/13	
US 20050228627 A1	System-level simulation of devices having diverse timing	20051013	703/13	
US 20050216594 A1	Instant messaging communications channel for transporting data between objects executing	20050929	709/227	
US 20050198606 A1	Compilation of remote procedure calls between a timed HDL model on a reconfigurable hard	20050908	716/18	
US 20050160345 A1	Apparatus, system, method and computer program product for reliable multicast transport of	20050721	714/776	
US 20050144585 A1	Method and system for hardware accelerated verification of digital circuit design and its testbe	20050630	716/18	
US 20050102125 A1	Inter-chip communication system	20050512	703/14	
US 20050086300 A1	Trust mechanism for a peer-to-peer network computing platform	20050421	709/204	
US 20040260528 A1	Co-simulation via boundary scan interface	20041223	703/14	
US 20040247316 A1	SYSTEM AND METHOD FOR PERFORMING IN-SERVICE FIBER OPTIC NETWORK CERT	20041209	398/47	
US 20040236556 A1	Multi-user server system and method	20041125	703/14	
US 20040181385 A1	HDL Co-simulation in a high-level modeling system	20040916	703/14	
US 20040162871 A1	Infrastructure for accessing a peer-to-peer network environment	20040819	709/201	
US 20040148326 A1	System and method for unique naming of resources in networked environments	20040729	709/200	
US 20040143801 A1	Structured algorithmic programming language approach to system design	20040722	716/3	
US 20040133640 A1	Presence detection using mobile agents in peer-to-peer networks	20040708	709/204	
US 20040111252 A1	Method and system for emulating a design under test associated with a test environment	20040610	703/28	
US 20040098447 A1	System and method for submitting and performing computational tasks in a distributed hetero	20040520	709/201	
US 20040088646 A1	Collaborative content coherence using mobile agents in peer-to-peer networks	20040506	715/500	
US 20040088369 A1	Peer trust evaluation using mobile agents in peer-to-peer networks	20040506	709/217	
US 20040088348 A1	Managing distribution of content using mobile agents in peer-topeer networks	20040506	709/202	
US 20040088347 A1	Mobile agents in peer-to-peer networks	20040506	709/202	
US 20040064693 A1	Distributed indexing of identity information in a peer-to-peer network	20040401	713/168	
US 20040064568 A1	Presence detection using distributed indexes in peer-to-peer networks	20040401	709/228	
US 20040064512 A1	Instant messaging using distributed indexes	20040401	709/206	
US 20040064511 A1	Peer-to-peer email messaging	20040401	709/206	
US 20040031038 A1	System and method for providing multiple embodiments of abstract software modules in peer	20040212	719/315	
US 20040030794 A1	System and method for multiplatform implementation of abstract software modules in peer-to-	20040212	709/230	
US 20040030743 A1	System and method for describing and identifying abstract software modules in peer-to-peer	20040212	709/203	
US 20040015816 A1	Coordination synthesis for software systems	20040122	717/101	
US 20030191615 A1	Synchronization of multiple simulation domains in an EDA simulation environment	20031009	703/13	
US 20030182421 A1	Distributed identities	20030925	709/224	

US 20030154465 A1	Method and system for verifying modules destined for generating circuits	20030814	717/137
US 20030154191 A1	Logical data modeling and integrated application framework	20030814	707/2
US 20030144828 A1	Hub array system and method	20030731	703/21
US 20030115564 A1	Block based design methodology	20030619	716/8
US 20030115147 A1	Secure access method and system	20030619	705/64
US 20030105620 A1	System, method and article of manufacture for interface constructs in a programming languag	20030605	703/22
US 20030095566 A1	Providing a camel based service to a subscriber terminal in a win network and vice versa	20030522	370/465
US 20030074178 A1	Emulation system with time-multiplexed interconnect	20030417	703/25
US 20030074177 A1	System, method and article of manufacture for a simulator plug-in for co-simulation purposes	20030417	703/22
US 20030070070 A1	Trust spectrum for certificate distribution in distributed peer-to-peer networks	20030410	713/157
US 20030055898 A1	Propagating and updating trust relationships in distributed peer-to-peer networks	20030320	709/205
US 20030055894 A1	Representing trust in distributed peer-to-peer networks	20030320	709/204
US 20030046671 A1	System, method and article of manufacture for signal constructs in a programming language	20030306	717/141
US 20030046668 A1	System, method and article of manufacture for distributing IP cores	20030306	717/131
US 20030041141 A1	Peer-to-peer presence detection	20030227	709/223
US 20030037321 A1	System, method and article of manufacture for extensions in a programming lanauage capab	20030220	717/149
US 20030033594 A1	System, method and article of manufacture for parameterized expression libraries	20030213	717/141
US 20030033588 A1	System, method and article of manufacture for using a library map to create and maintain IP	20030213	717/107
US 20030028864 A1	System, method and article of manufacture for successive compilations using incomplete par	20030206	717/141
US 20030028585 A1	Distributed trust mechanism for decentralized networks	20030206	709/201
US 20030002521 A1	Bootstrapping for joining the peer-to-peer environment	20030102	370/465
US 20020199173 A1	System, method and article of manufacture for a debugger capable of operating across multip	20021226	717/129
US 20020194581 A1	Methods and systems for automatically translating geometric data	20021219	717/136
US 20020188657 A1	Resource identifiers for a peer-to-peer environment	20021212	709/201
US 20020184358 A1	Peer-to-peer communication pipes	20021205	709/223
US 20020184357 A1	Rendezvous for locating peer-to-peer resources	20021205	709/223
US 20020184311 A1	Peer-to-peer network computing platform	20021205	709/204
US 20020184310 A1	Providing peer groups in a peer-to-peer environment	20021205	709/204
US 20020177990 A1	Distributed logic analyzer for use in a hardware logic emulation system	20021128	703/28
US 20020166098 A1	Block based design methodology	20021107	716/1
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US 20040260528 A	Co-simulation system for interfacing high-level modeling system for electronic circuit design, I	20041223	
WO 2003012640 A	Behavior processor system for operating portion of user design and interfacing with host test	20030213	
GB 2370134 A	Digital circuit co-simulation method for integrated circuit designing involves converting model	20020619	

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L1	128	co-simulation or cosimulation	US-PGPUB
L2	622	high level near2 (model$3)	US-PGPUB
L3	738	1 or 2	US-PGPUB
L4	1	3 and (boundary-scan near2 (interface or protocol))	US-PGPUB
L5	6	3 and ("boundary scan" near2 (interface or protocol))	US-PGPUB
L6	6	4 or 5	US-PGPUB
L7	10	3 and (interface with((reconfigurable or re-configurable) near2 hardware))	US-PGPUB
L8	1	3 and ("boundary scan" with translat$3)	US-PGPUB
L9	10	6 or 7 or 8	US-PGPUB
L10	1	9 and (protocol.CLM.)	US-PGPUB
L11	1	9 and ("boundary scan".CLM.)	US-PGPUB
L12	6	9 and ("reconfigurable".CLM.)	US-PGPUB

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US 20050256691 A1	Post initial microcode load co-simulation method, system, and program product	20051117	703/14	
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US 20050228627 A1	System-level simulation of devices having diverse timing	20051013	703/13	
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US 20050102125 A1	Inter-chip communication system	20050512	703/14	

US 20050086300 A1	Trust mechanism for a peer-to-peer network computing platform	20050421	709/204
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US 20040162871 A1	Infrastructure for accessing a peer-to-peer network environment	20040819	709/201
US 20040148326 A1	System and method for unique naming of resources in networked environments	20040729	709/200
US 20040143801 A1	Structured algorithmic programming language approach to system design	20040722	716/3
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US 20040088347 A1	Mobile agents in peer-to-peer networks	20040506	709/202
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US 20040064511 A1	Peer-to-peer email messaging	20040401	709/206
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US 20040030794 A1	System and method for multiplatform implementation of abstract software modules in peer-to	20040212	709/230
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US 20030055898 A1	Propagating and updating trust relationships in distributed peer-to-peer networks	20030320	709/205
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US 5870588 A	Design environment and a design method for hardware/software co-design	19990209	703/13
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